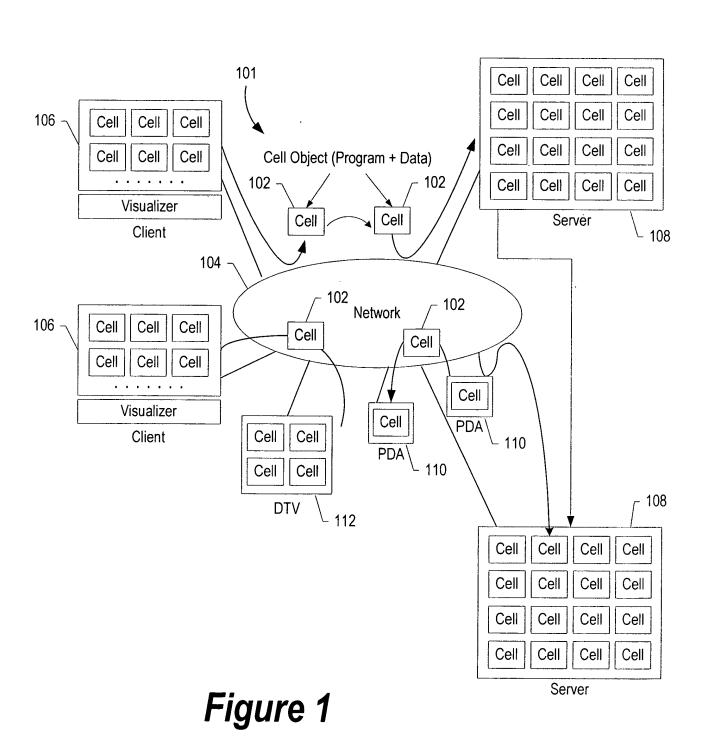
EL978317440US

File: 3017.VSD

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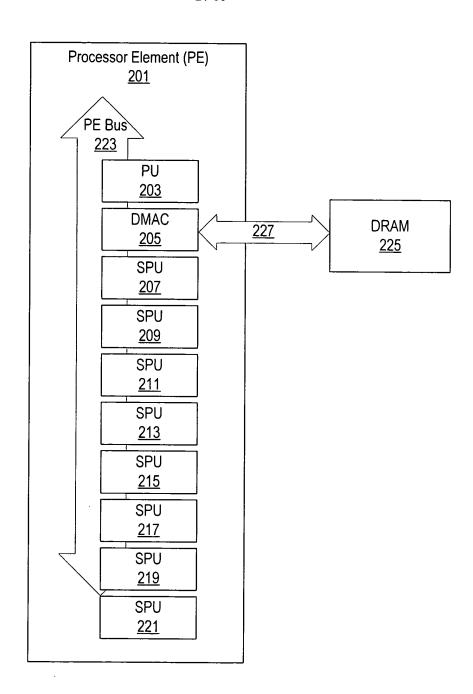


Figure 2

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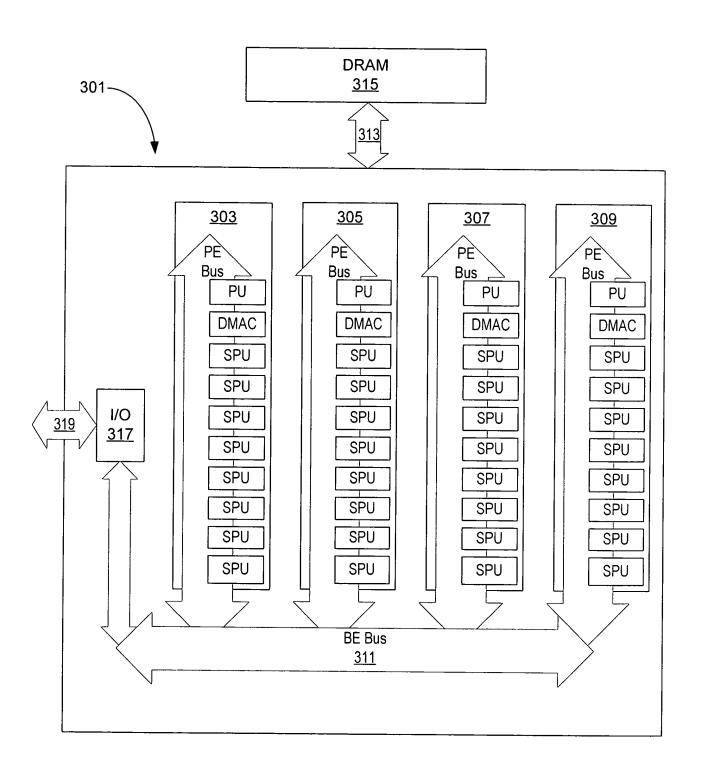


Figure 3

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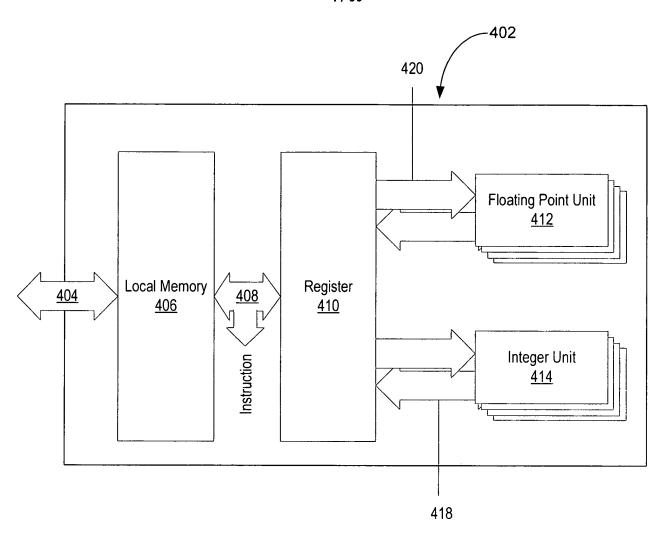


Figure 4

File: 3017.VSD Docket No. AUS920030402US1
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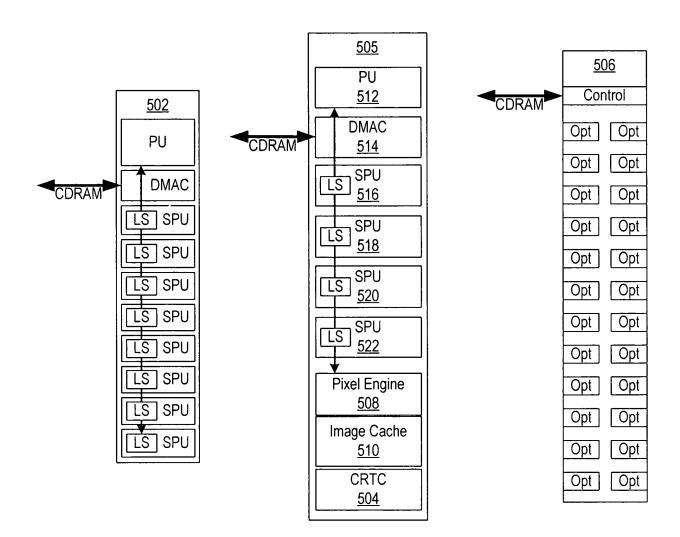
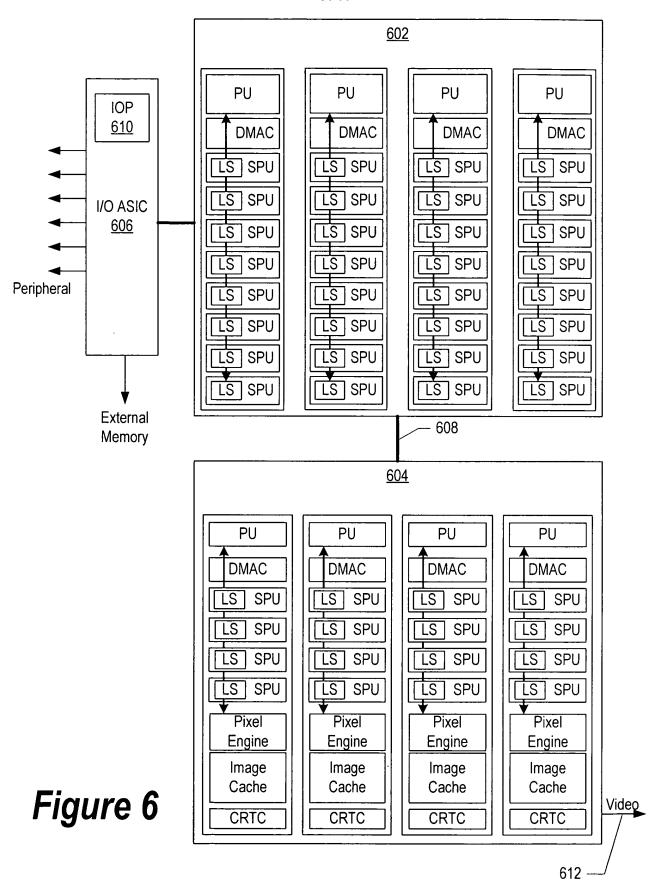


Figure 5

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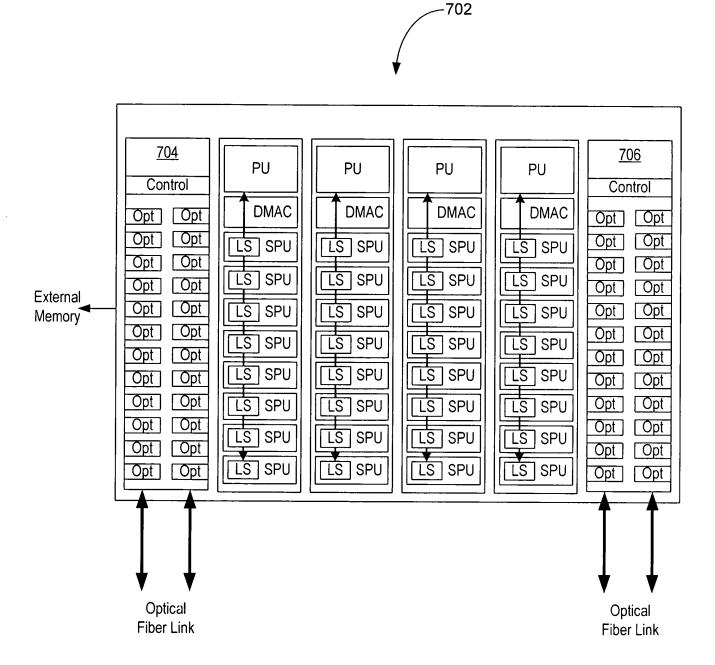


Figure 7

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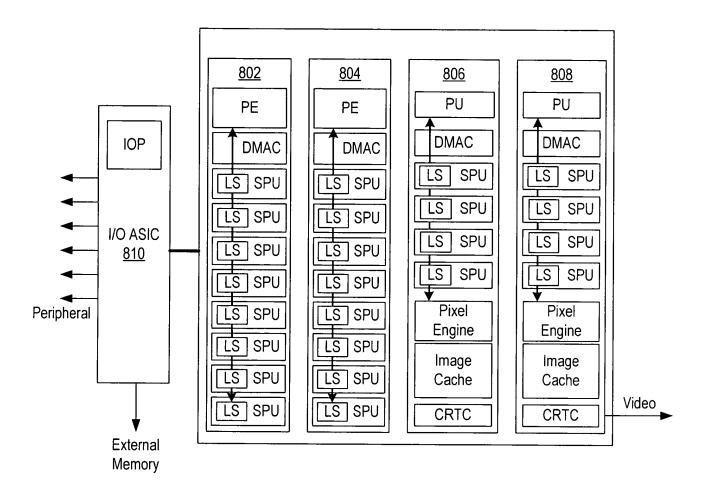


Figure 8

File: 3017.VSD Docket No. AUS920030402US1
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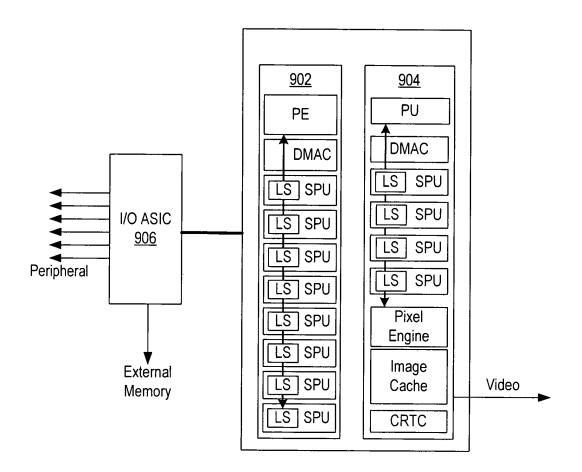


Figure 9

File: 3017.VSD Docket No. AUS920030402US1
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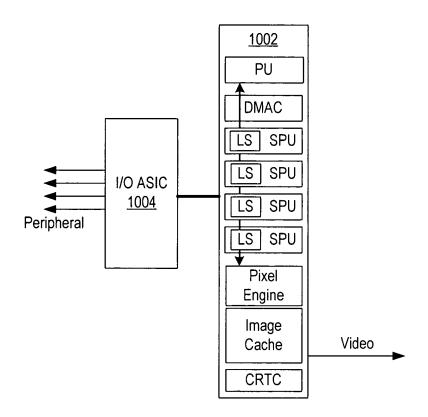
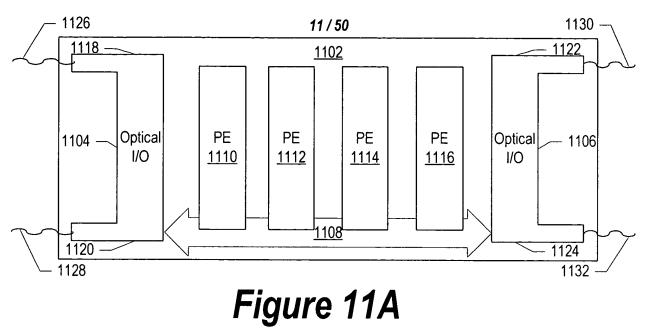
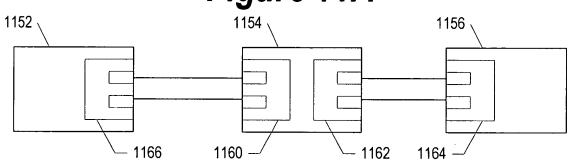
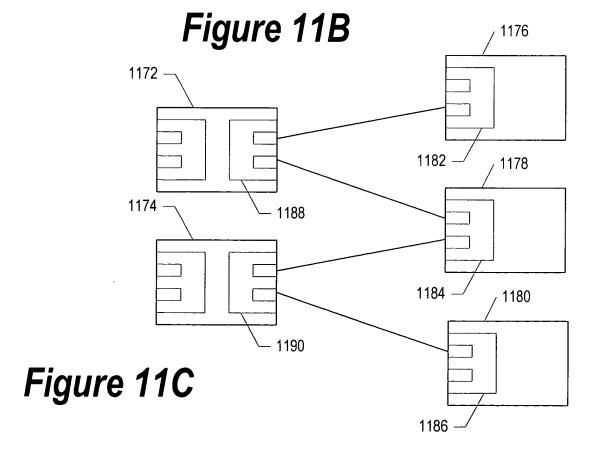


Figure 10

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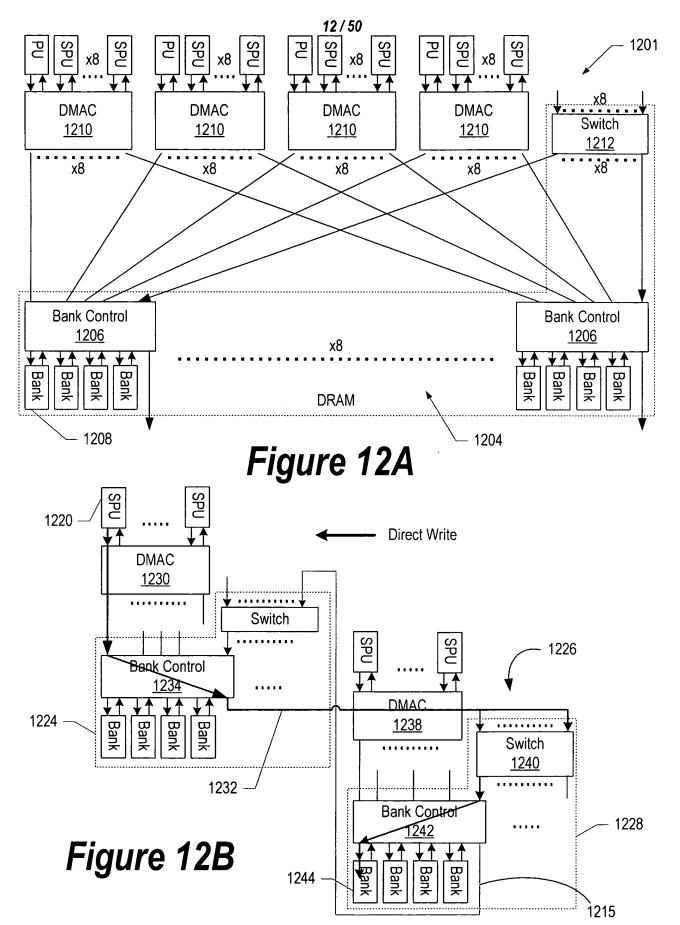




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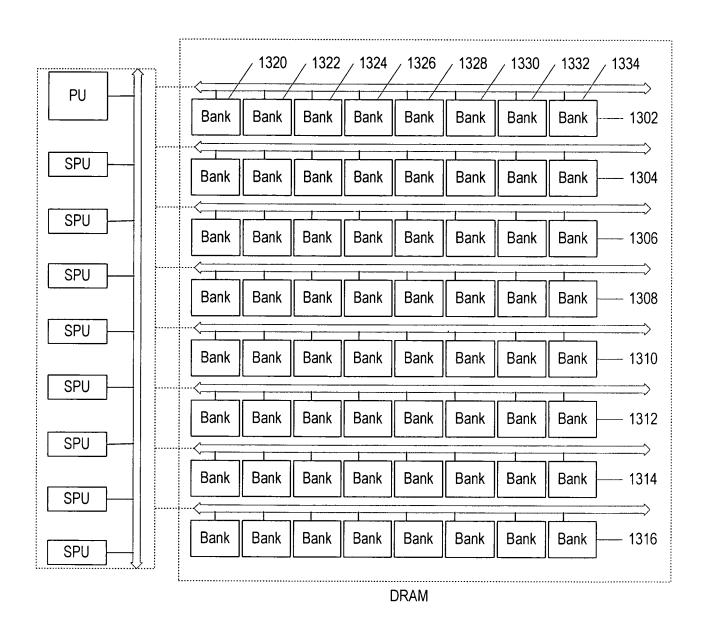


Figure 13

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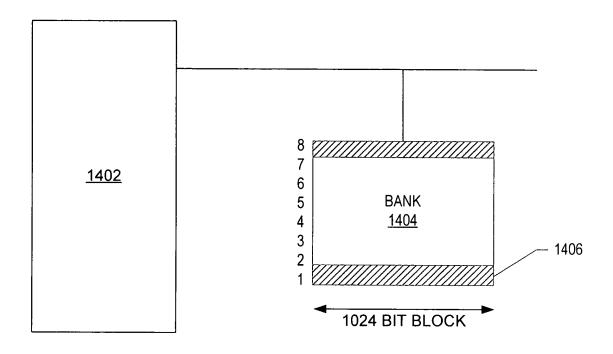


Figure 14A

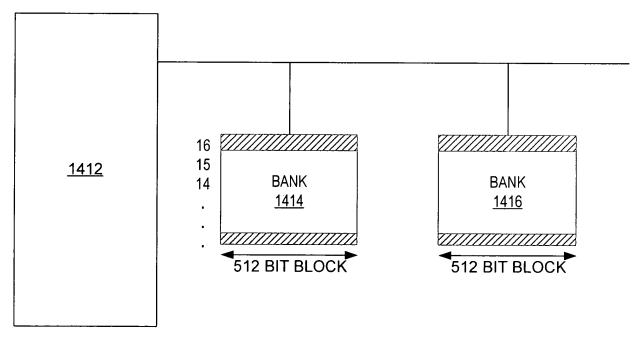


Figure 14B

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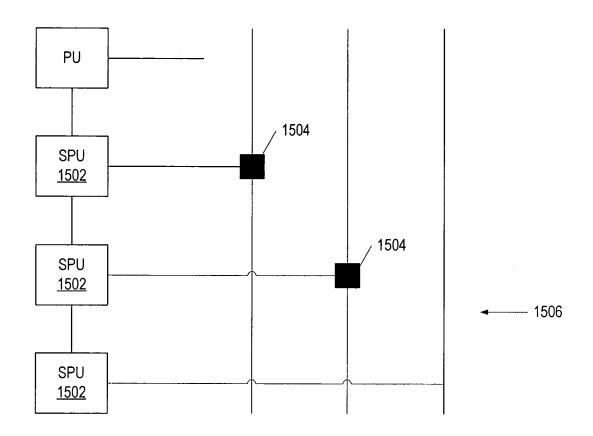


Figure 15

Docket No. AUS920030402US1

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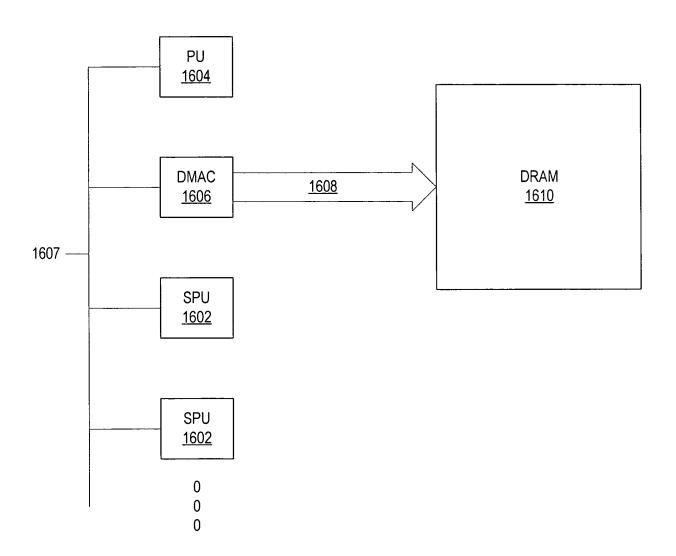
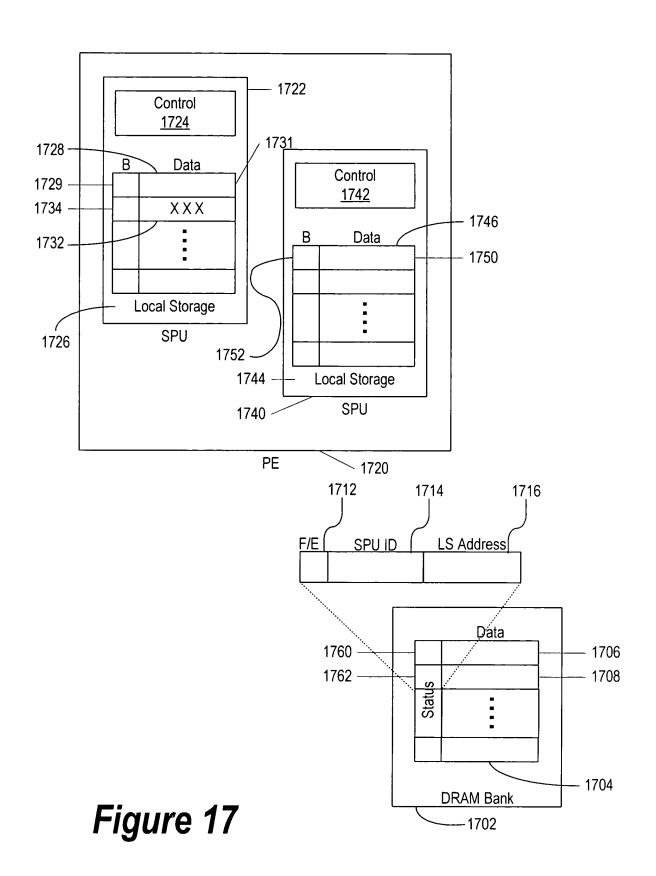


Figure 16

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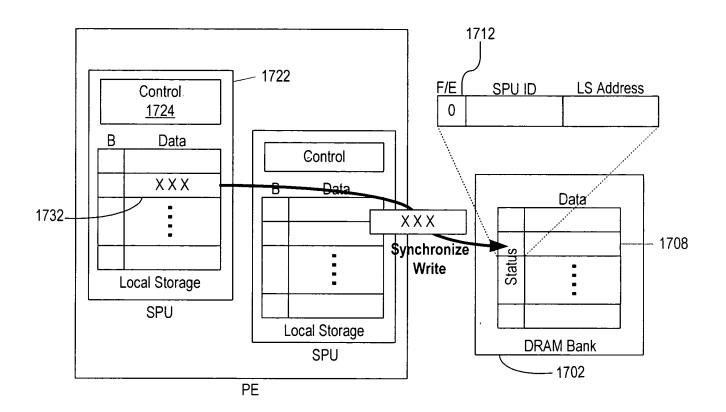


Figure 18

D cket No. AUS920030402US1 Hofst , t al. System and M thod for Sharing Memory by Heterogeneous Processors

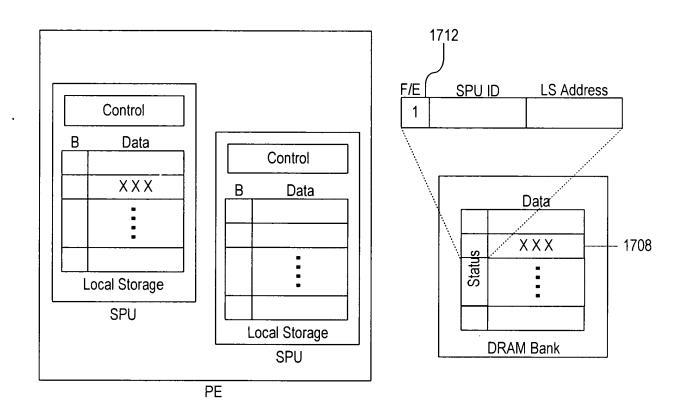
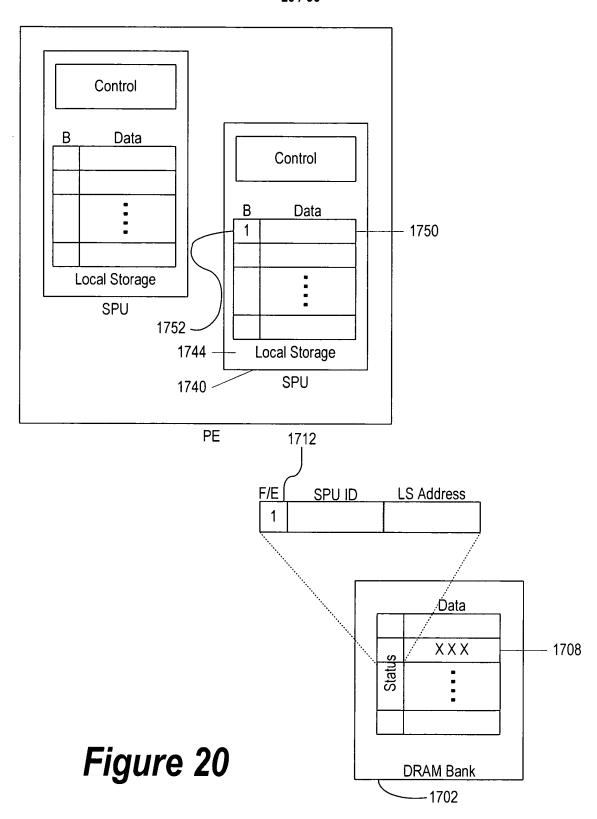


Figure 19

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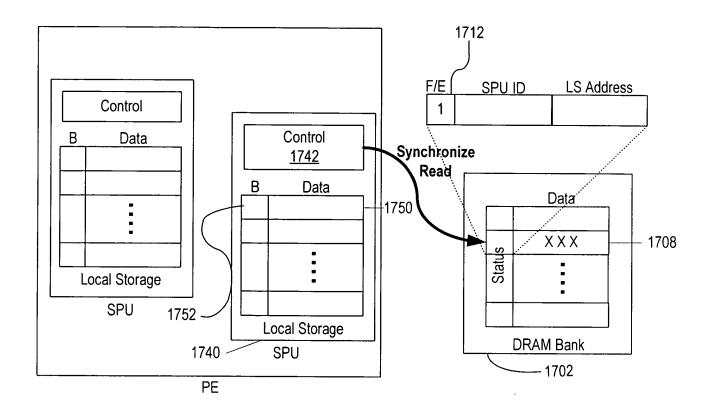
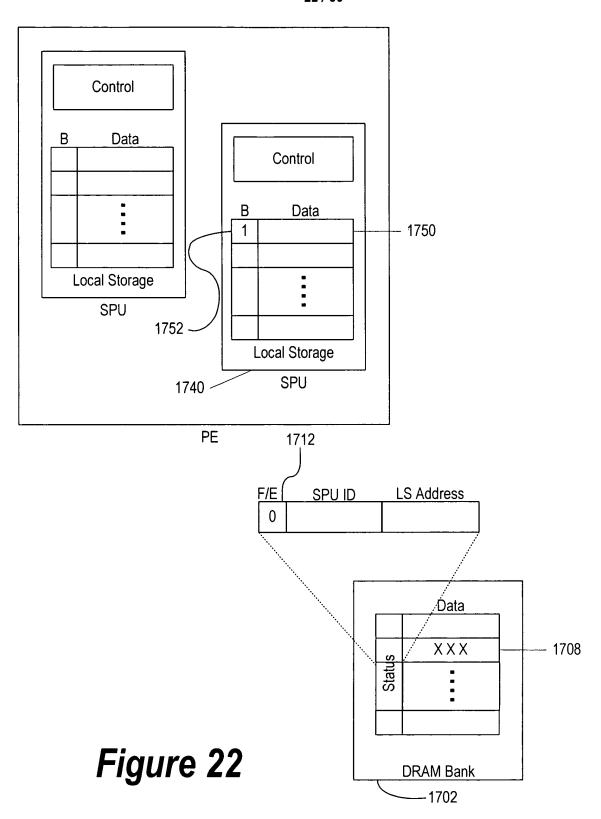


Figure 21

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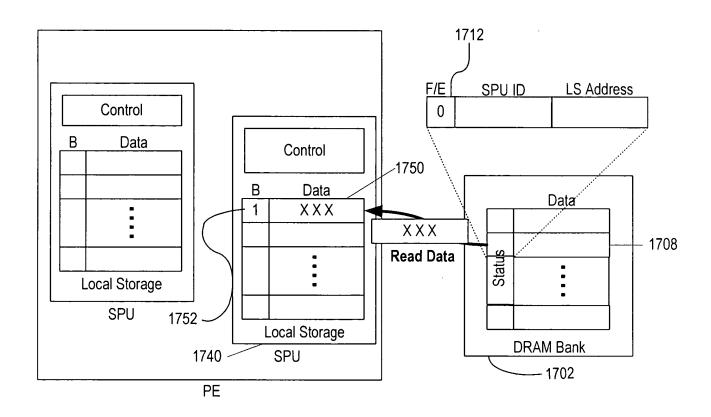
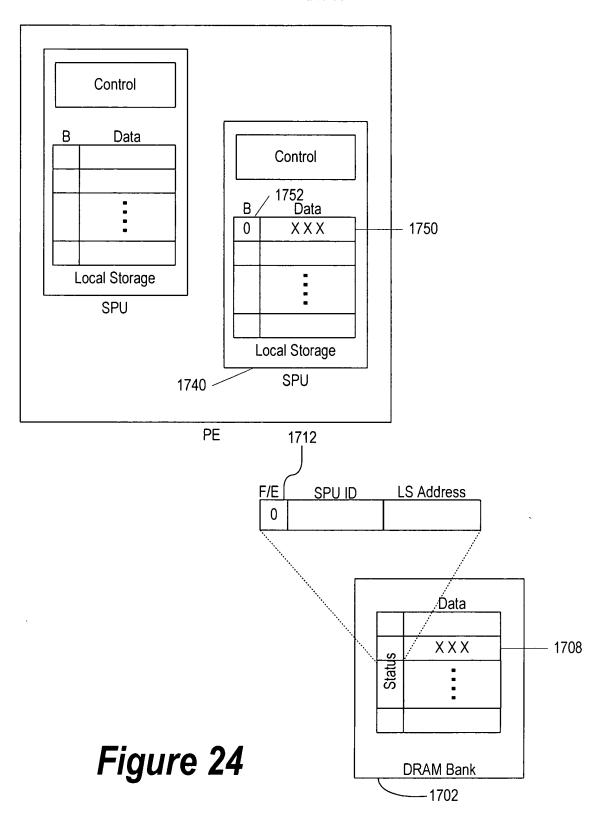


Figure 23

Dock t No. AUS920030402US1 Hofst , et al. System and Method for Sharing M mory by Heterogen ous Processors

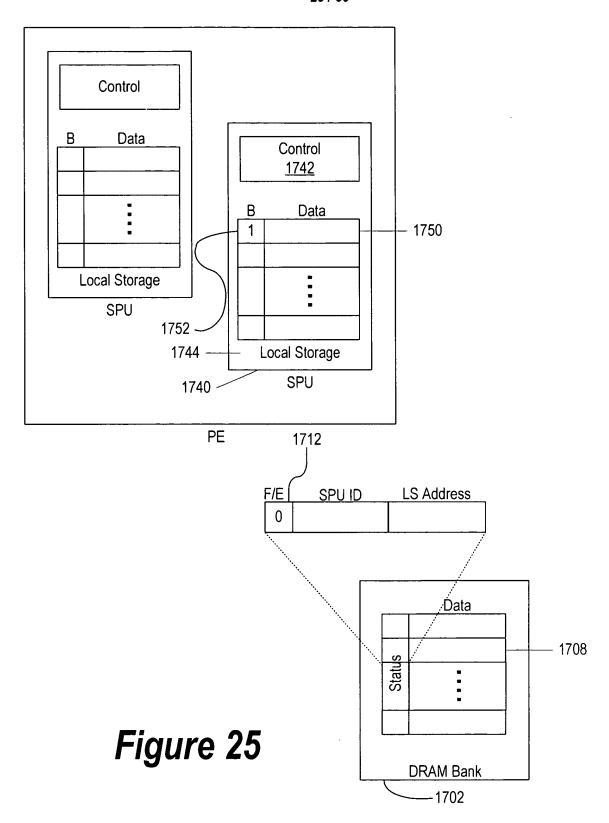


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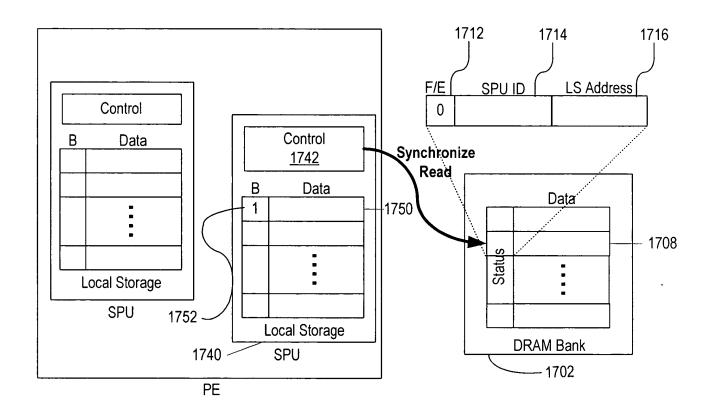


Figure 26

Docket No. AUS920030402US1 Hofst , et al. Syst m and Method for Sharing Memory by Heterogeneous Proc ssors

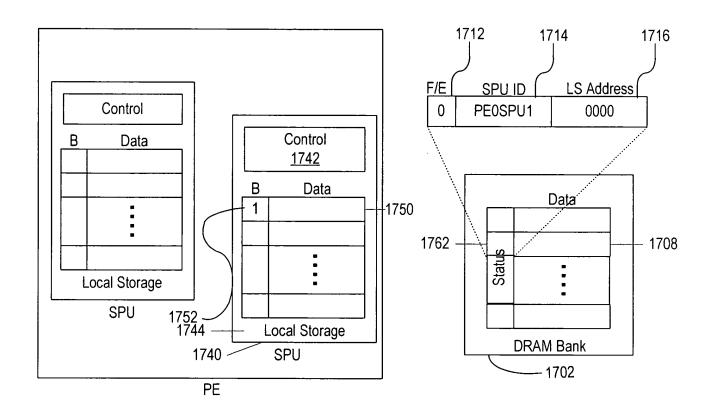


Figure 27

Docket No. AUS920030402US1 Hofstee, et al. System and Method for Sharing Memory by Heterogeneous Processors

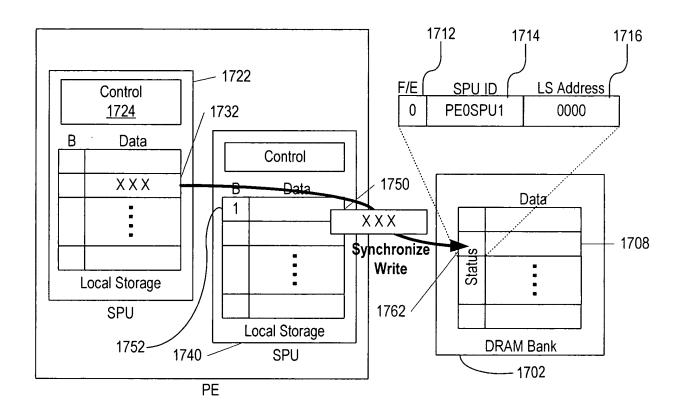


Figure 28

Docket No. AUS920030402US1 Hofstee, et al. System and Method for Sharing Memory by Heterogeneous Processors

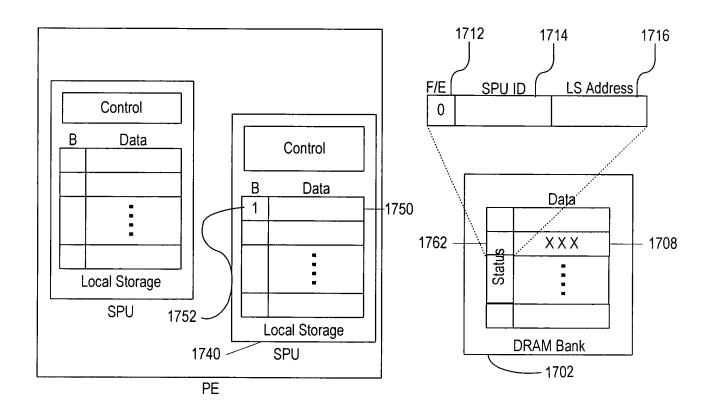


Figure 29

Dock t No. AUS920030402US1 Hofstee, et al. System and M thod for Sharing Memory by H terogeneous Processors

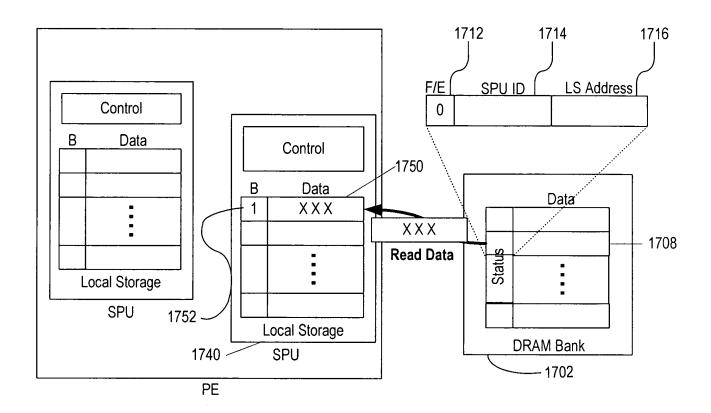


Figure 30

Docket No. AUS920030402US1 Hofstee, et al. System and M thod for Sharing Memory by Heterogen ous Proc ssors

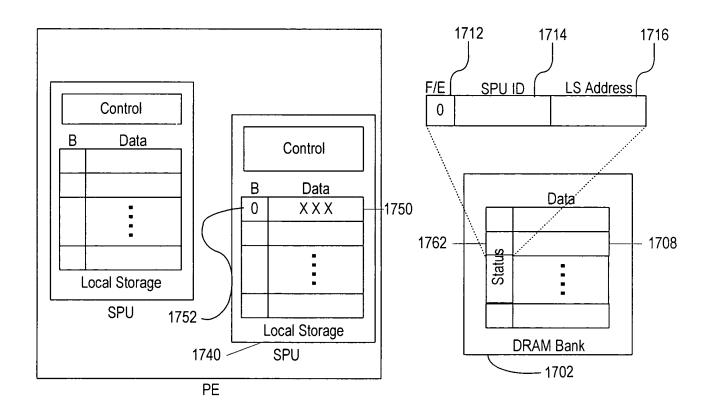


Figure 31

File: 3017.VSD Docket No. AUS920030402US1
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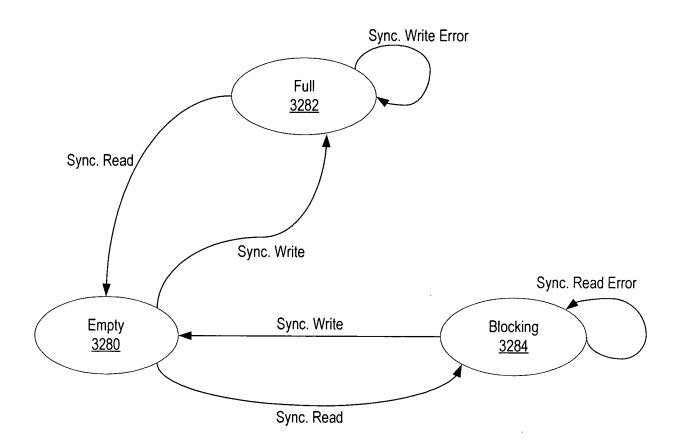


Figure 32

Docket No. AUS920030402US1

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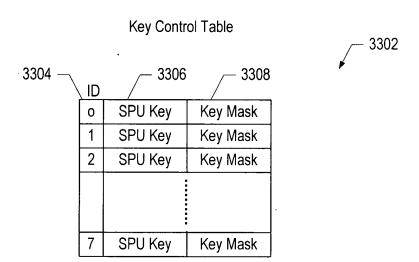


Figure 33

Docket No. AUS920030402US1

Hofste , t al.

System and M thod for Sharing Memory by Het rogeneous Proc ssors

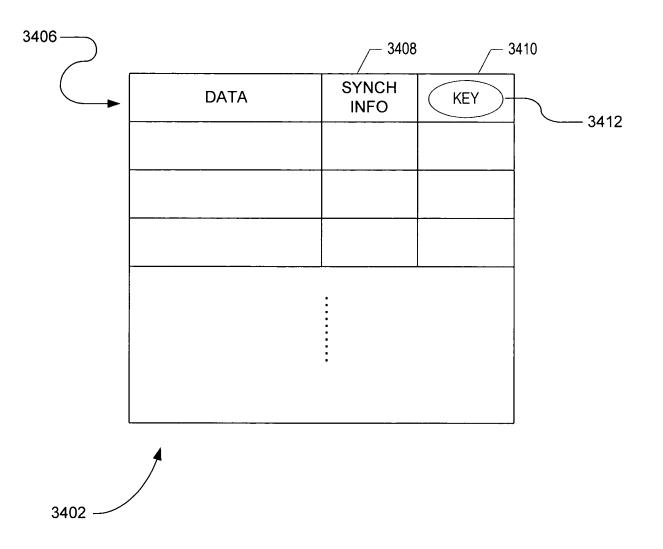


Figure 34

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Memory Access Control Table

2504		3506	_ 35	508 3510	3512
3504 —	∖ID				
	0	Base	Size	Access Key	Access Key Mask
	1	Base	Size	Access Key	Access Key Mask
	2	Base	Size	Access Key	Access Key Mask
			•		
	63	Base	Size	Access Key	Access Key Mask



Figure 35

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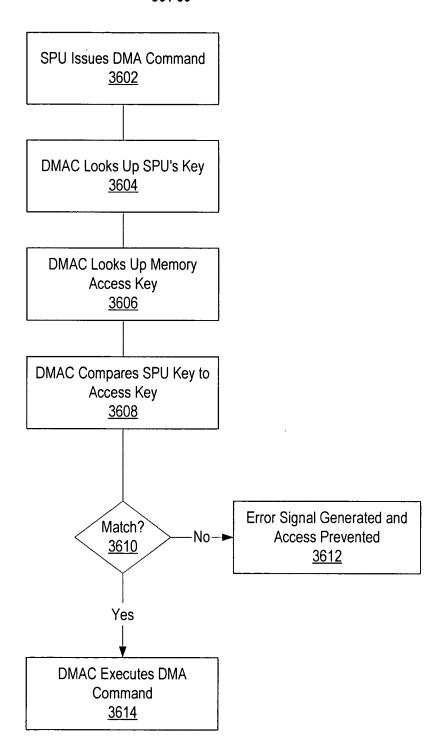


Figure 36

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Syst m and M thod for Sharing Memory by H t rog n ous Processors

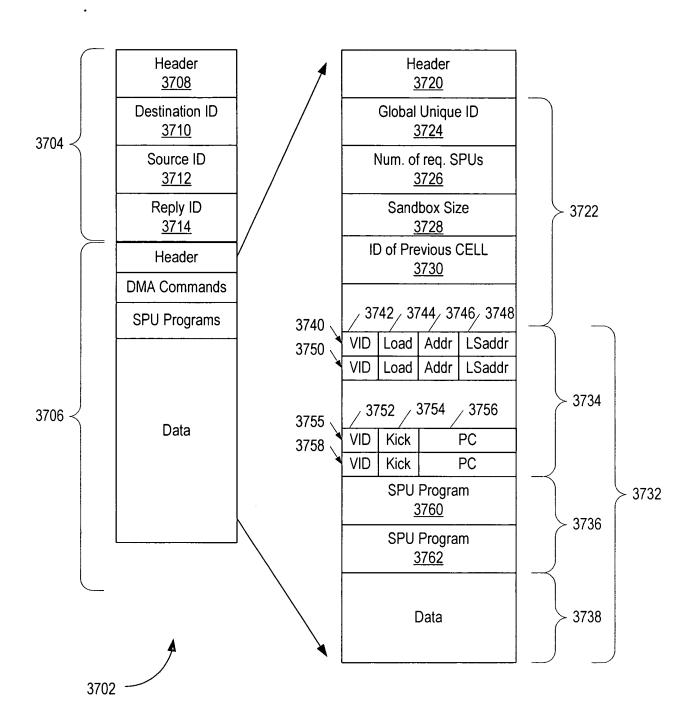


Figure 37

Docket No. AUS920030402US1 Hofstee, et al. System and Method for Sharing Memory by Heterogeneous Proc ssors

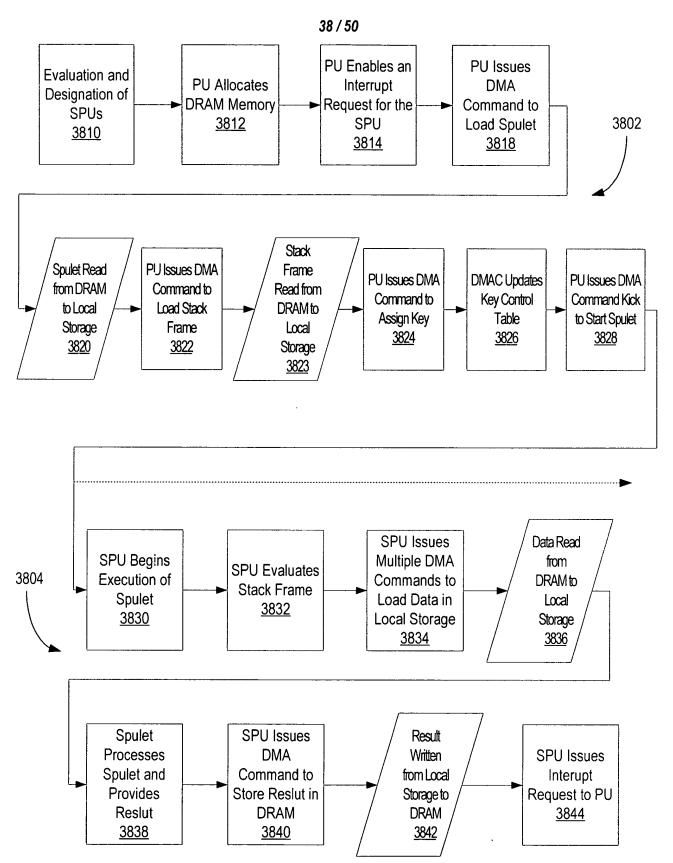
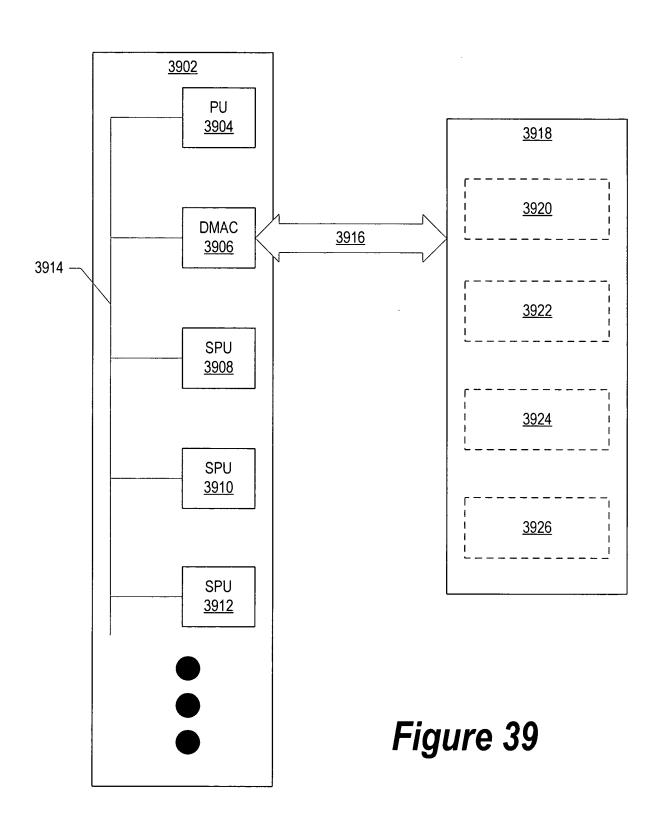


Figure 38

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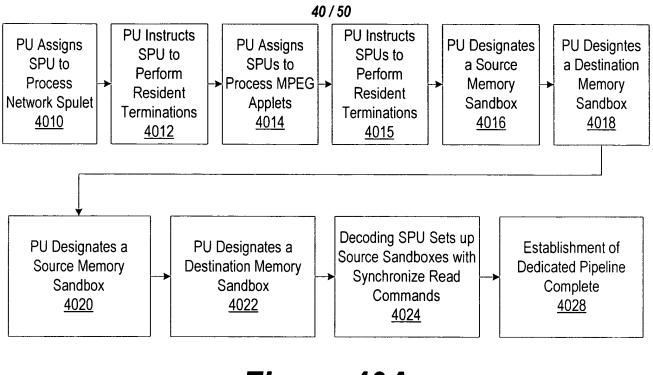


Figure 40A

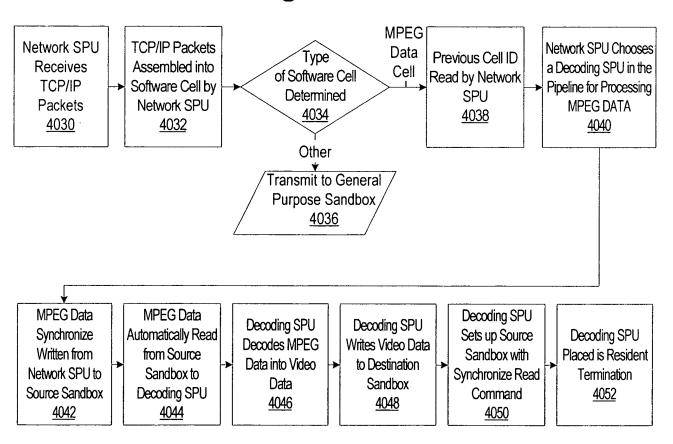


Figure 40B

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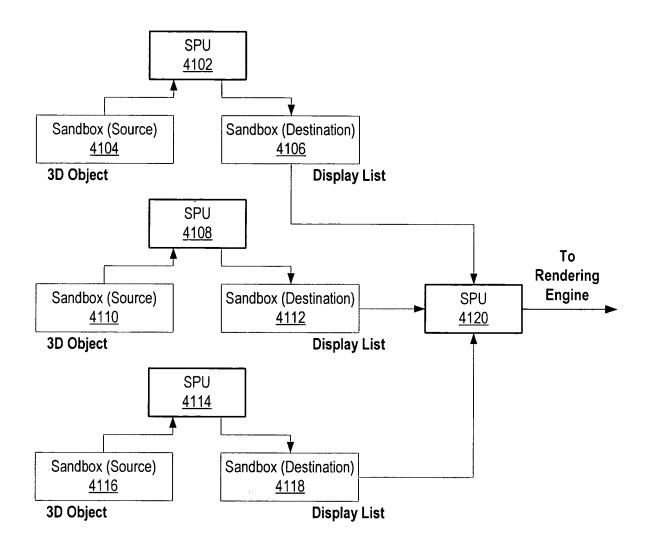


Figure 41

Dock t No. AUS920030402US1

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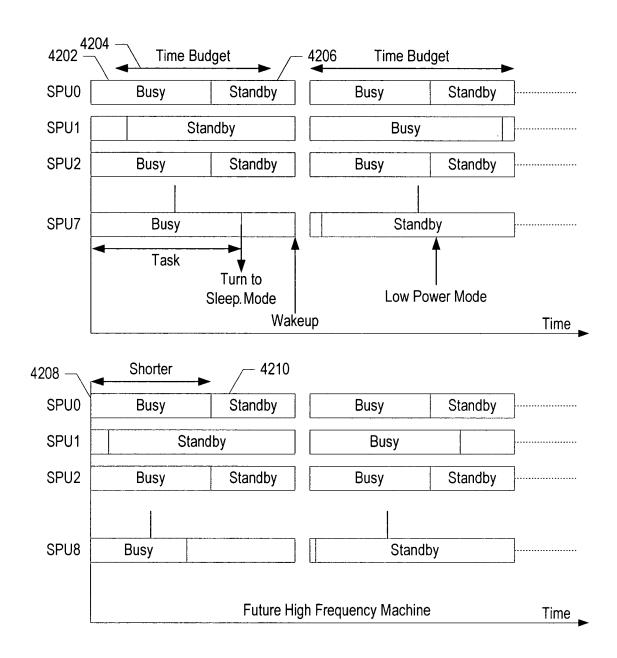


Figure 42

Docket No. AUS920030402US1

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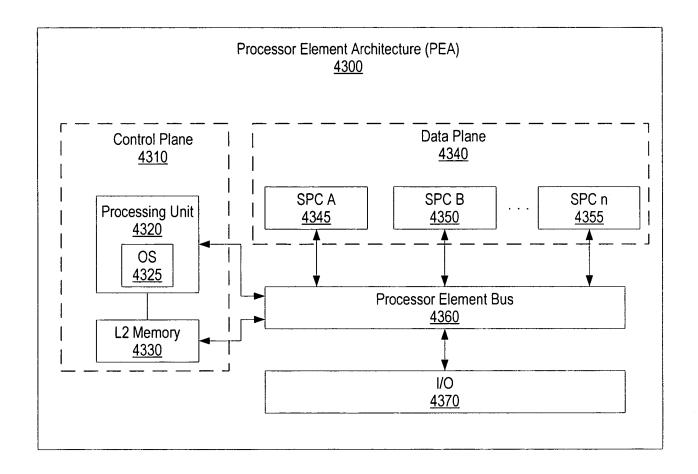


Figure 43

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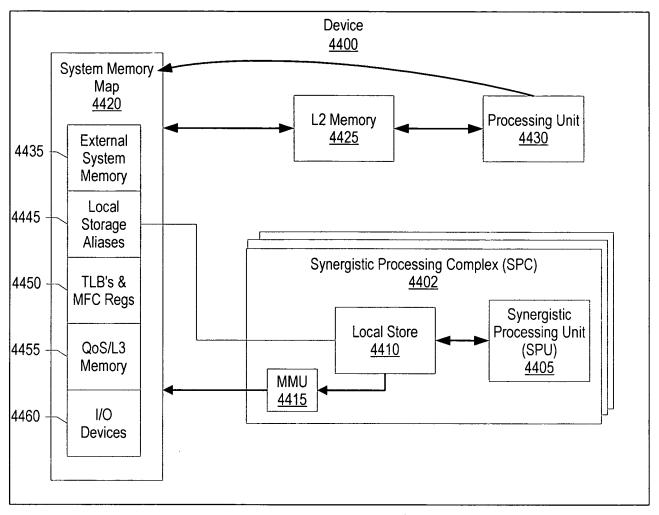


Figure 44A

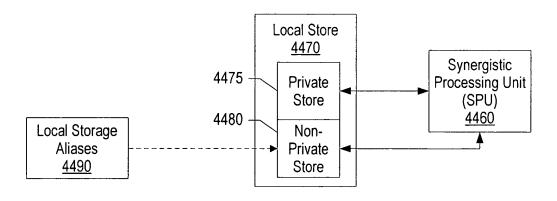


Figure 44B

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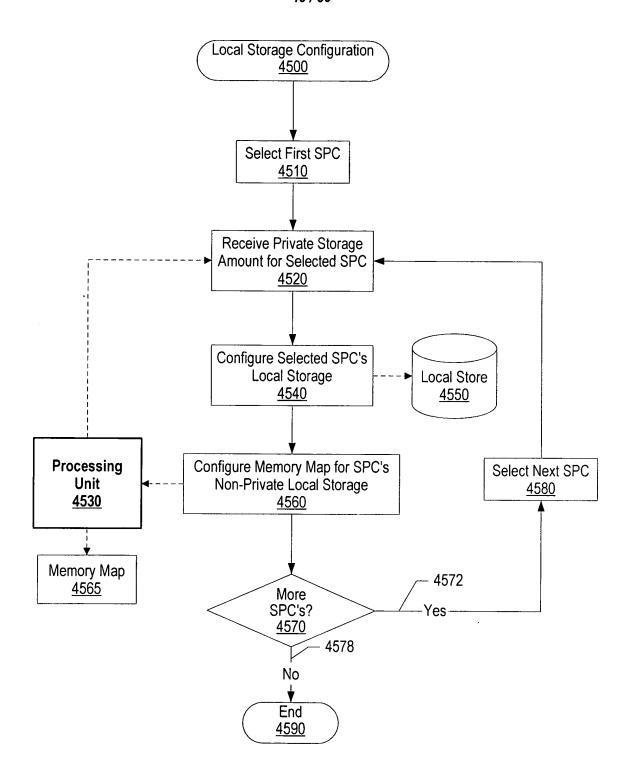


Figure 45

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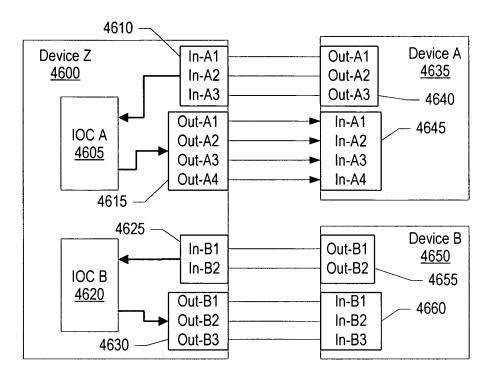


Figure 46A (Prior Art)

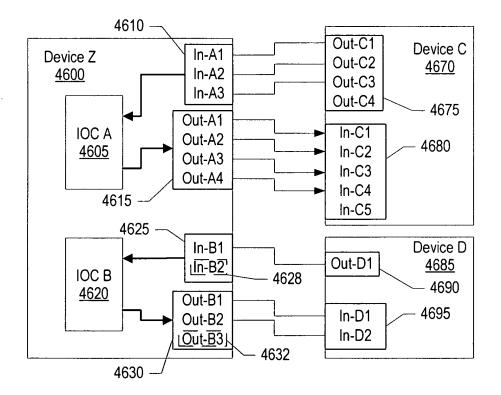


Figure 46B (Prior Art)

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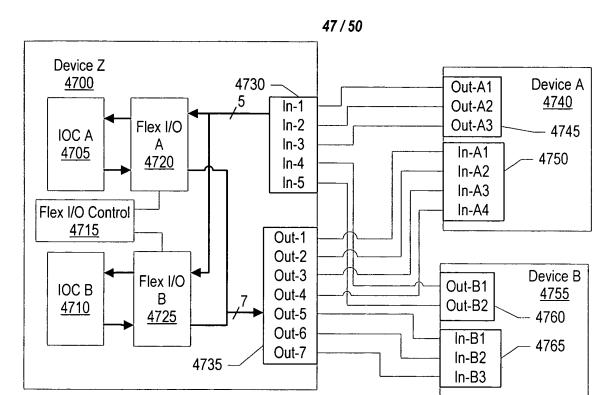


Figure 47A

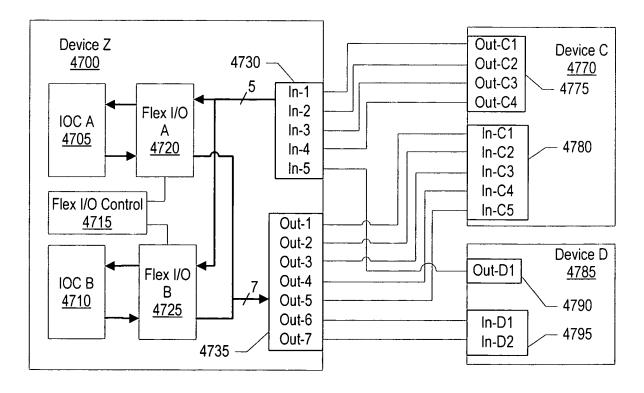


Figure 47B

Docket No. AUS920030402US1 Hofstee, et al. System and M thod for Sharing M mory by H terogen ous Processors

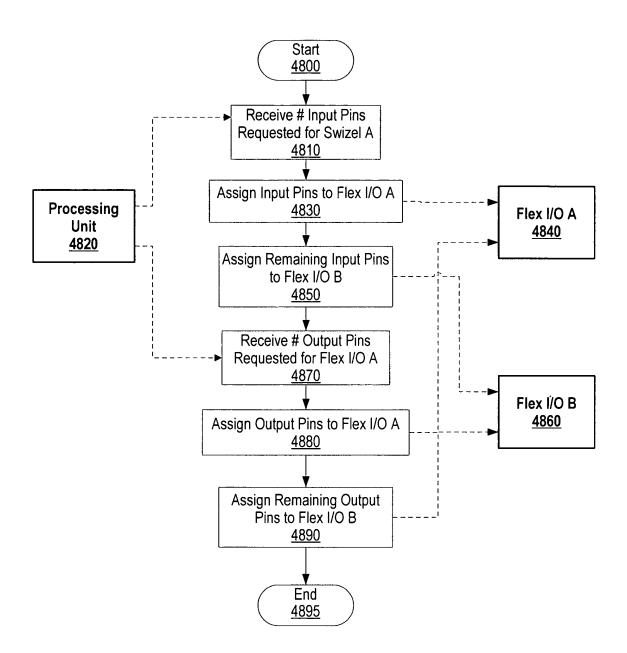
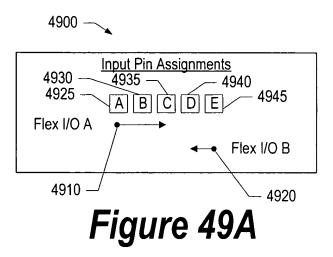


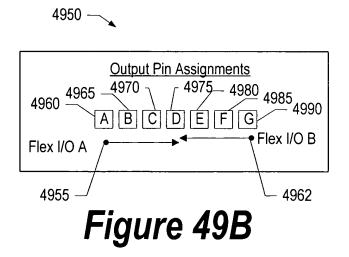
Figure 48

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System and Method for Sharing Memory by H t rogeneous Processors

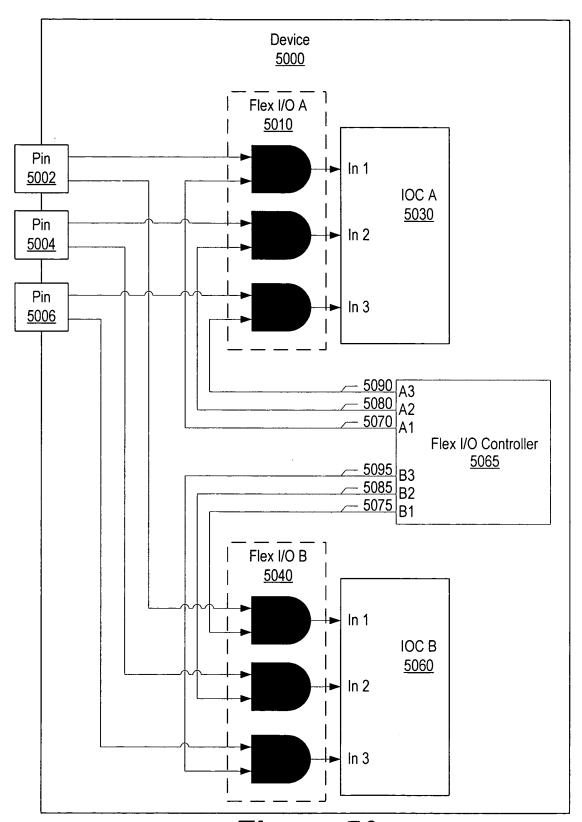


Figure 50